|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Instruction | Reg Write | ALUSrc | MemWrite | MemtoReg | ALUCtrl | Branch | immChk |
| halt |  |  | 0 |  | Xx |  | 0 |
| lw | 1 |  | 0 | 1 | Xx |  | 0 |
| bgtR1 |  |  | 0 |  | Xx | 1 | 0 |
| sw |  |  | 1 |  | xx |  | 1 |
| sub | 1 |  | 0 |  | 01 |  | 0 |
| shiftL | 1 |  | 0 |  | 10 |  | 0 |
| shiftR | 1 |  | 0 |  | 11 |  | 0 |
| init | 1 |  | 0 |  | xx |  | 1 |
| add | 1 |  | 0 |  | 00 |  | 0 |
| bltR1 |  |  | 0 |  |  | 1 | 0 |
| addi | 1 | 1 | 0 |  |  |  | 0 |
| j |  |  | 0 |  |  | 1 | 0 |